

## **Monitor Binary for SPCI2S, SPCI4 and CPM8**

### **Release Notes for Version 1.04**

#### **1. Introduction**

The main purpose of this release is to support 48 kbit/s operation. A problem that could cause the board to restart under load is also corrected. This is the first release of the software since V1.02 and it is backwards compatible with that release.

#### **2. New functionality**

##### **2.1 Support for 48kbit/s operation**

This release supports 48kbit/s signalling link operation. 48kbit/s mode is selected by the Board Configuration Request (MGT\_MSG\_CONFIG0). The data rate is selected by the per-link flags (**link $n$ \_flags**) field as follows:

<b>Bit 11</b>	<b>Bit 10</b>	<b>Data Rate</b>
0	0	64kbit/s
0	1	48kbit/s
1	1	56kbit/s

#### **3. Faults cleared**

##### **3.1 Board Restart**

In certain hardware configurations the card was restarting when operating under moderate to heavy load. This was due to a problem on the PCI bus interface. This release contains a modification to correct the restart problem.

## **Monitor Binary for SPCI2S, SPCI4 and CPM8**

### **Release Notes for Version 1.05**

#### **1. Introduction**

This release addresses a potential intermittent problem which could lead to messages no longer being reported for some links.

The release is fully backwards compatible with the previous release.

#### **2. Faults cleared**

##### **2.1 Intermittent failure to report further messages**

A problem identified with previous releases that caused some links to stop reporting monitored messages altogether has been corrected.

The problem would show up occasionally on some but not all boards. When it occurred the result was usually that monitoring operation of the first 4 links being monitored ceased. Other links were not impacted.

Intel Corporation  
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## **Monitor Binary for SPCI2S, SPCI4 and CPM8**

### **Release Notes for Version 1.06**

#### **1. Introduction**

This release contains an update to the correction implemented in V1.05 to ensure that the problem is correctly addressed in all cases.

The release is fully backwards compatible with the previous release.

#### **2. Faults cleared**

##### **2.1 Intermittent failure to report further messages**

A problem identified with previous releases that caused some links to stop reporting monitored messages altogether on SPCI4 and SPCI2S boards has been corrected.

Intel Corporation  
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